

## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. Dox 1450 Alexandris, Vignica 22313-1450 www.usplu.gov



Bib Data Sheet

**CONFIRMATION NO. 4084** 

| SERIAL NUMBE<br>09/978,141   | FILING OR 371(c) DATE 10/15/2001 RULE   | <b>CLASS</b><br>716    | GROUP AR'<br>2825     | UP ART UNIT<br>2825   |                            | ATTORNEY<br>DOCKET NO.<br>01-181 |  |
|--|---|------------------------|-----------------------|---|----------------------------|----------------------------------|--|
| APPLICANTS Yaron Kretchmer, Fremont, CA; Michael Porter, Milpitas, CA; Thomas Obrien, Fort Collins, CO;  ** CONTINUING DATA ********************************** |   |                        |                       |   |                            |                                  |  |
| Foreign Priority claimed<br>35 USC 119 (a-d) cond<br>met<br>Verified and<br>Acknowledged<br>ADDRESS  | STATE OR COUNTRY CA   | SHEETS<br>DRAWING<br>4 | TOTAL<br>CLAIMS<br>29 |   | INDEPENDENT<br>CLAIMS<br>5 |                                  |  |
| 024319 TITLE AUTOMATIC METHOD AND SYSTEM FOR INSTANTIATING BUILT-IN -TEST (BIST) MODULES IN ASIC MEMORY DESIGNS  |   |                        |                       |   |                            |                                  |  |
| FILING FEE F   | FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following: |                        |                       | All Fees  1.16 Fees (Filing)  1.17 Fees (Processing Ext. of time)  1.18 Fees (Issue)  Other  Credit |                            |                                  |  |